

SDET (Software Development Engineer in Test) Lead

Flex Logix develops industry-leading AI inference engines and we are the #1 provider of eFPGA solutions. Our InferX X1 is the industry's fastest and most-efficient AI edge inference accelerator that brings AI to high-volume applications, surpassing competitor's performance at 1/7th size and much lower price. InferX X1 is available as a chip, PCIe board and M.2 board. InferX X1 is working and running YOLOv3 today and starts production shipments this autumn. Our Inference Compiler is easy to use (we take in neural network models in TensorFlowLite) and our APIs for Infer X1 allow rapid integration by the customer. Our architecture is covered by dozens of patents and is highly differentiated giving us a sustainable competitive advantage.

We recently completed a \$55M funding round.

Flex Logix is expanding rapidly and is seeking a leader for SDET. Our products are very cost effective and also very high performance. The hardware and software need to work together seamlessly and efficiently to achieve this. The right candidate needs to be able to develop test automation and lead test planning while bootstrapping this new team. The right candidate will be also working the development team to raise the quality on our short term releases.

Responsibilities

- Review and create test plans to support releases
- Develop test processes
- Developing tests
- Operate tests and collect quality metrics to support releases
- Develop and maintain test automation for inference software products.
- Build the team. You will be the first employee of a new team in a rapidly growing startup.

Read more about InferX on our Inference page at www.flex-logix.com

EXPERIENCE AND SKILL REQUIRED

- 10+ years of experience in test or test automation
- Fluent in Python
- Highly skilled at system level debug in Linux based systems
- Management and/or leadership experience
- Understands how to build and manage automation for system testing.
- Must understand how to build a test plan for a new product.

Additional qualifications for an ideal candidate:

- Experience with AI
- Experience writing C++ applications

Must be very smart and very motivated, must be a quick learner, proactive and curious.

Must be passionate about being part of an aggressive, venture-backed startup team that is changing the way chips are architected, designed, and programmed

Must be entrepreneurial, innovative problem solver and willing to work hard.

Must live in Silicon Valley or Austin TX. Strong preference for US citizenship or permanent residency ("green card"); will consider candidates with current H1-B visas who are willing to transfer promptly.