

Job Description

Senior/Principal Hardware Verification Engineer

RESPONSIBILITIES

Verification Engineer involved in functional verification and customer delivery of the EFLX (embedded FPGA) cores in different process nodes (most recent designs in 14/16nm, moving to 7nm and beyond). Responsible for all aspects of verification and emulation including:

- Work with Architecture, RTL and Physical Design Engineers
- Setup of industry standard Verification IP flow
- Point of contact for verification related customer issues and deliverables
- Development of verification testbenches and debug for EFLX IP
- Functional and GLS verification using Verilog simulator, regressions and coverage closure
- JTAG/DFT/ATPG verification and pattern generation
- Development of emulation testbenches using standard emulation toolsets (Mentor Graphics' Veloce, Cadence's Palladium, etc.)
- Development of testbench and debug for silicon validation, post-silicon bring-up and checkout; Linux-based validation using C++/Python or similar
- Development of coverage plans and metrics, drive coverage activities and test writing
- UPF simulation and debug
- Post-Silicon pattern generation and testing using system bench setup to validate EFLX core
- Power/Performance benchmarking and debug

EXPERIENCE AND SKILL REQUIRED

- BSEE/MSEE with at least 5 years of relevant industry experience
- Must be very smart and very motivated
- Hands-on experience in VIP development, setup of verification tools and methodology such as SystemVerilog or UVM/OVM and in developing verification plans
- Hands-on functional verification planning, coverage analysis, coverage closure, and assertion implementation
- Hands-on functional coverage analysis and assertion implementation experience
- Hands-on experience with standard functional simulators such as NCSIM or Questa
- Preferred experience OR willing to quickly learn:
 - FPGA debug exposure
 - Emulation flow development in Mentor Graphics' Veloce or equivalent emulation hardware
 - Worked with and directed external contractors

Must be passionate about being part of an aggressive, venture-backed startup team that is changing chip architecture. Must be entrepreneurial, innovative problem solver and willing to work hard.

MUST live in Austin, TX or Silicon Valley and have US citizenship or permanent residency

("green card"), or holding a current H1-B visa

Compensation

Flex Logix will offer a competitive compensation package for the successful candidate, including salary, equity, and benefits.