

Job Description

Hardware Verification Engineer (Entry/Senior/Principal)

RESPONSIBILITIES

Verification Engineer involved in functional verification and emulation of Inference SoC and EFLX (embedded FPGA) cores in different process nodes. Responsible for all aspects of verification and emulation including:

- Integration of industry standard Verification IPs
- Development and debug of UVM/SV testbenches for SoCs and TPU
- Bring-up and integration verification of cores, NoC, LPDDR4X/5 memory, PCIe, USB, DFT subsystems
- Development of verification testbench for silicon validation, post-silicon bring-up and checkout; Linux-based validation using C++/Python or similar
- Development of coverage plans and metrics, drive coverage activities and test writing
- Gate-Level Simulation/UPF simulation and debug
- Emulation of SoC and/or TPU using an industry standard emulation tool
- Silicon On-tester pattern generation using standard IP functional features

EXPERIENCE AND SKILL REQUIRED

- BSEE/MSEE with at least 5 years of relevant industry experience: your title will be based on your current level and experience
- Must be very smart and very motivated
- Must have hands-on experience in VIP setup/integration of tools from Synopsys, Avery, SmartDV or similar
- Must have hands-on experience with UVM/OVM
- Must have hands-on experience in developing verification plans for SoC or ASIC architectures
- Must have hands-on, test-writing experience with SIMD, RISC-V or ARM ISA, AMBA, JTAG/DFT architectures
- Must have hands-on functional coverage analysis and assertion implementation experience
- Must have hands-on experience with standard functional simulators such as NCSIM or Questa
- Preferred experience OR willing to quickly learn:
 - FPGA debug exposure
 - LPDDR4X/5, PCIe5/USB4 architecture
 - Emulation flow development in Mentor Graphics' Veloce or equivalent emulation hardware
 - Exposure to Formal Verification techniques
 - Worked with and directed external contractors

Must be passionate about being part of an aggressive, venture-backed startup team that is changing chip architecture. Must be entrepreneurial, innovative problem solver and willing to work hard.

MUST live in Austin, TX or Silicon Valley and have US citizenship or permanent residency (“green card”), or holding a current H1-B visa

Compensation

Flex Logix will offer a competitive compensation package for the successful candidate, including salary, equity, and benefits.